

10/27/66

**Gustavo Siller, Jr., Registration No. 32,305**

Name of applicant, assignee or Registered Representative

Signature \_\_\_\_\_

Date of Signature

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

For: DIRECT CONVERSION CIRCUIT  
HAVING REDUCED BIT ERRORS

)  
)  
) Examiner: Nguyen Thanh Vo  
)  
) Group Art Unit No. 2618  
)  
)  
)  
)

Remarks begin on page 5 of this paper.